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jc997 U.S. PTO  
10/075972  
02/14/02



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant** : Terry L. Frueling et al. :  
:  
**Serial No.** : Not Yet Assigned (CIP of 60/269,065; :  
09/309,055; 09/309,135 and 09/309,054) :  
:  
:  
**Attny No.** : 60,408-126 :  
:  
:  
**Filed** : Concurrently Herewith :  
:  
:  
**Title** : SECURED MICROCONTROLLER :  
ARCHITECTURE :

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**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.97 AND 1.98**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to Rule 97 and in compliance with Rule 56, Applicant hereby makes of record the items listed on the attached Form PTO-1449. Copies of the listed items are enclosed.

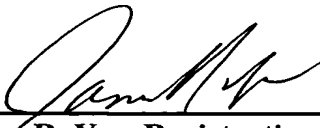
None of these items discloses the inventive concepts claimed in the subject application.

Should there be a fee associated with this document, please charge it to Deposit Account No. 08-2789. A duplicate of this paper is enclosed.

**Respectfully submitted,**

**HOWARD & HOWARD ATTORNEYS, P.C.**

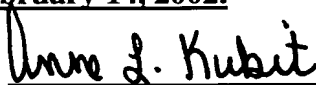
2/14/02  
Date

  
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Enclosures:  
PTO-1449 w/ 16 Cited References

**CERTIFICATE OF EXPRESS MAILING**

I hereby certify that the enclosed paper or fee is being deposited with the United States Postal Service as Express Mail, postage prepaid, in an envelope as "Express Mail Post Office to Addressee", Mailing Label No. EL831186514US and addressed to the Assistant Commissioner of Patents, Washington, D. C. 20231, on February 14, 2002.



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Anne L. Kubit



Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 60,408-126	SERIAL NO.
	APPLICANT: Fruehling et al.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	FILING DATE	GROUP:

10/075972



## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.							Date	Name	Class	Sub-Class	Filing Date
	5	1	9	3	1	8	9	03/09/93	Flood et al.	395	650	12/09/91
	5	4	0	4	3	0	4	04/04/95	Wise et al.	364	426.04	11/19/93
	5	4	5	9	7	3	2	10/17/95	Wise et al.	364	426.02	11/19/93
	5	8	2	5	7	8	5	10/20/98	Barry et al.	371	22.4	5/24/96
	5	8	4	1	8	6	7	11/24/98	Jacobson et al.	380	25	11/01/96
	6	0	0	6	3	4	5	12/11/99	Berry Jr.	714	718	05/09/97
	6	0	6	5	1	3	5	05/16/00	Marshall et al.	714	11	06/04/99
	6	1	7	0	0	2	5	01/02/01	Drott et al.	710	48	08/11/98

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document No.							Date	Name	Class	Sub-Class	Filing Date

## OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)

AA	J. Sosnowski, Concurrent Error Detection Using Signature Monitors. Proc of Fault Tolerant Computing Systems, Methods, Applications. 4th International GI/ITG/GMA Conference, Sept. 1989, pp 343-355
AB	K. Wilken, J.P. Shen, "Continuous Signature Monitoring: Low-Cost Concurrent Detection of Processor Control Errors, "IEEE Transactions on Computer-Aided Design, Vol. 9, pp. 629-641 June 1990.
AC	Intel Corporation, Embedded PentiumR Processor Family Developer's Manual, "Error Detection," Chapter 22, pp. 393-399

Examiner	Date Considered
Examiner: Initial if references considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. 60,408-126		SERIAL NO.		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				APPLICANT: Fruehling				
				FILING DATE		GROUP:		
<b>U.S. PATENT DOCUMENTS</b>								
Examiner Initial	Document No.			Date	Name	Class	Sub- Class	Filing Date
<b>FOREIGN PATENT DOCUMENTS</b>								
Examiner Initial	Document No.			Date	Name	Class	Sub- Class	Filing Date
<b>OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)</b>								
AD	E. Bohl, T. Lindenkreuz, and R. Stephan, "The Fail-Stop Controller AE11, "Proc. International Test Conference, IEEE Computer Society Press, Los Alamitos, Calif., 1997, pp. 567-577							
AE	U.S. Patent Application "METHOD AND CIRCUIT FOR ANALYSIS OF THE OPERATION OF A MICROCONTROLLER USING SIGNATURE ANALYSIS OF MEMORY" . Serial No.: 09/309,055 Filed May 10, 1999							
AF	U.S. Patent Application "METHOD AND CIRCUIT FOR ANALYSIS OF THE OPERATION OF A MICROCONTROLLER USING SIGNATURE ANALYSIS OF DATA AND INSTRUCTIONS" Serial No.: 09/309,135 Filed May 10, 1999							
AG	U.S. Patent Application "METHOD AND CIRCUIT FOR ANALYSIS OF THE OPEATION OF A MICROCONTROLLER USING SIGNATURE ANALYSIS DURING OPERATION Serial No.: 09/309,054 Filed May 10, 1999							
AH	DELPHI SECURED MICROCONTROLLER ARCHITECTURE by Terry L. Fruehling, SAE 2000 World Congress, March 6-9 2000, pp.1-12							
Examiner				Date Considered				